

A Major-Project report on
**“SOURCE CURRENT RIPPLE REDUCTION WITH INPUT VOLTAGE
CONTROLLED LED DRIVER”**

Submitted to

Jawaharlal Nehru Technological University, Hyderabad

In partial fulfillment of the academic requirements for
the award of Degree of

BACHELOR OF TECHNOLOGY

In

ELECTRICAL & ELECTRONICS ENGINEERING

By

K.SINDHUJA

206Y5A0213

P.SRILATHA

206Y5A0219

D.VARSHITHA

196Y1A0203

T.ANUSHA

196Y1A0217

Under the esteemed guidance of

Mr.R. Shashi Kumar Reddy M. Tech

Assistant Professor



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



2022-23

Rajini
PRINCIPAL

Sumathi Reddy Institute of Technology for Women
Ananthasagar (V), Hasanparthy (M)
WARANGAL - 506 371 (T.S.)



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING



CERTIFICATE

This is to certify that the major project entitled **“SOURCE CURRENT RIPPLE REDUCTION WITH INPUT VOLTAGE CONTROLLED LED DRIVER”** submitted to JNTUH carried out by the following students of IV-B.Tech in the partial fulfillment for the award of the B.Tech Degree in **Electrical & Electronics Engineering** during the academic year 2022-23.

K.SINDHUJA

P.SRILATHA

D.VARSHITHA

T.ANUSHA

R. S. K. Reddy
Guide

Mr. R. Shashi Kumar Reddy

206Y5A0213

206Y5A0219

196Y1A0203

196Y1A0217

[Signature]
Head of the Dept

Dr. K. Mahender Sharma



Rajini

PRINCIPAL

Sumathi Reddy Institute of Technology for Women
Ananthasagar (V), Hasanparthy (M)
WARANGAL - 506 371 (T.S.)

ABSTRACT

With the increase in the power demand for lighting applications, efficiency concerns and developments in LED lighting, majority of lighting applications are becoming LED based. Photo-voltaic (PV) source based industrial or commercial lighting has considerable importance. PV based systems give better performance if the current drawn from it has reduced ripple current. This paper presents an LED driver configuration for PV based systems such that source current ripple can be reduced. A three-phase series resonant converter is used for obtaining the DC output voltage for LED load. The load voltage is regulated by controlling the input voltage of the inverter. A controlled voltage is derived and added to the PV source for regulating the total input to the converter system. The input to the inverter configuration is always maintained to be constant irrespective of increase or decrease in the PV source. A buck-boost converter is used on the source side for deriving controlled voltage for regulation of input voltage to the three-phase series resonant converter feeding the LED load. For dimming the illumination, low frequency PWM technique is used. The proposed converter is studied using MATLAB simulation for 100.3W of output power. The converter is expected to give high efficiency and can handle large lighting loads with reduced ripple in the source current.



Rijan

Principal

Sumathi Reddy Institute of Technology for Women
Ananthasagar (V), Hasanparthy (M)
WARANGAL - 506 371 (TS)