### III YEAR I SEMESTER

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### III YEAR II SEMESTER

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During Summer Vacation between III and IV Years: Industry Oriented Mini Project
**Professional Elective – I**

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<td>Spread Spectrum Communications</td>
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<td>Digital system Design</td>
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*Open Elective* subjects’ syllabus is provided in a separate document.

*Open Elective* – Students should take Open Electives from the List of Open Electives Offered by Other Departments/Branches Only.

**Ex:** - A Student of Mechanical Engineering can take Open Electives from all other departments/branches except Open Electives offered by Mechanical Engineering Dept.
ELECTROMAGNETIC THEORY AND TRANSMISSION LINES

B.Tech. III Year I Sem. Course Code: EC501PC

Course Objectives:
This is a structured foundation course, dealing with concepts, formulations and applications of Electromagnetic Theory and Transmission Lines, and is the basic primer for all electronic communication engineering subjects. The main objectives of the course are:

- To learn the Basic Laws, Concepts and proofs related to Electrostatic Fields and Magnetostatic Fields, and apply them to solve physics and engineering problems.
- To distinguish between static and time-varying fields, and understand the significance and utility of Maxwell’s Equations and Boundary Conditions, and gain ability to provide solutions to communication engineering problems.
- To analyze the characteristics of Uniform Plane Waves (UPW), determine their propagation parameters and estimate the same for dielectric and dissipative media.
- To conceptually understand the UPW Polarization features and Poynting Theorem, and apply them for practical problems.
- To determine the basic Transmission Line Equations and telephone line parameters and estimate the distortions present.
- To understand the concepts of RF Lines and their characteristics, Smith Chart and its applications, acquire knowledge to configure circuit elements, QWTs and HWTs, and to apply the same for practical problems.

Course Outcomes: Having gone through this foundation course, the students would be able to

- Distinguish between the static and time-varying fields, establish the corresponding sets of Maxwell’s Equations and Boundary Conditions, and use them for solving engineering problems.
- Analyze the Wave Equations for good conductors and good dielectrics, and evaluate the UPW Characteristics for several practical media of interest.
- Establish the proof and estimate the polarization features, reflection and transmission coefficients for UPW propagation, distinguish between Brewster and Critical Angles, and acquire knowledge of their applications.
- Determine the Transmission Line parameters for different lines, characterize the distortions and estimate the characteristics for different lines.
- Analyze the RF Line features and configure them as SC, OC Lines, QWTs and HWTs, and design the same for effective impedance transformation.
- Study the Smith Chart profile and stub matching features, and gain ability to practically use the same for solving practical problems.
UNIT – I
**Electrostatics:** Coulomb’s Law, Electric Field Intensity – Fields due to Different Charge Distributions, Electric Flux Density, Gauss Law and Applications, Electric Potential, Relations Between E and V, Maxwell’s Two Equations for Electrostatic Fields, Energy Density, Illustrative Problems. Convection and Conduction Currents, Dielectric Constant, Isotropic and Homogeneous Dielectrics, Continuity Equation, Relaxation Time, Poisson’s and Laplace’s Equations; Capacitance – Parallel Plate, Coaxial, Spherical Capacitors, Illustrative Problems.

UNIT – II


UNIT – III


UNIT – IV

UNIT – V
**Transmission Lines – II:** Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR. UHF Lines as Circuit Elements; λ/4, λ/2, λ/8 Lines – Impedance Transformations, Significance of $Z_{\text{min}}$ and $Z_{\text{max}}$, Smith Chart – Configuration and Applications, Single Matching, Illustrative Problems.
TEXT BOOKS:

REFERENCE BOOKS:
LINEAR AND DIGITAL IC APPLICATIONS

B.Tech. III Year I Sem.
Course Code: EC502PC

Course Objectives:
1. The main objectives of the course are:
2. To introduce the basic building blocks of linear integrated circuits.
3. To teach the linear and non-linear applications of operational amplifiers.
4. To introduce the theory and applications of analog multipliers and PLL.
5. To teach the theory of ADC and DAC.
6. To introduce the concepts of waveform generation and introduce some special function ICs.
7. To understand and implement the working of basic digital circuits.

Course Outcomes: On completion of this course, the students will have:
1. A thorough understanding of operational amplifiers with linear integrated circuits.
2. Understanding of the different families of digital integrated circuits and their characteristics.
3. Also students will be able to design circuits using operational amplifiers for various applications.

UNIT - I

UNIT - II

UNIT - III
Data Converters: Introduction, Basic DAC techniques, Different types of DACs-Weighted resistor DAC, R-2R ladder DAC, Inverted R-2R DAC, Different Types of ADCs - Parallel Comparator Type ADC, Counter Type ADC, Successive Approximation ADC and Dual Slope ADC, DAC and ADC Specifications.
UNIT - IV

**Digital Integrated Circuits:** Classification of Integrated Circuits, Comparison of Various Logic Families Combinational Logic ICs – Specifications and Applications of TTL-74XX & Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers, Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers, Parallel Binary Adder/Subtractor, Magnitude Comparators.

UNIT - V

**Sequential Logic IC’s and Memories:** Familiarity with commonly available 74XX & CMOS 40XX Series ICs – All Types of Flip-flops, Synchronous Counters, Decade Counters, Shift Registers.
Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.

**TEXT BOOKS:**

**REFERENCE BOOKS:**
4. Linear Integrated Circuits and Applications – Salivahanan, MC GRAW HILL EDUCATION.
DIGITAL COMMUNICATIONS

B.Tech. III Year I Sem.                                      L    T    P   C
Course Code: EC503PC                          4     1    0    4

Course Objectives:
- To understand the functional block diagram of Digital communication system.
- To understand the need for source and channel coding.
- To study various source and channel coding techniques.
- To understand a mathematical model of digital communication system for bit error rate analysis of different digital communication systems.

Course Outcomes: At the end of the course, the student will be able to:
- Design optimum receiver for Digital Modulation techniques.
- Analyze the error performance of Digital Modulation Techniques.
- Understand the redundancy present in Digital Communication by using various source coding techniques.
- Know about different error detecting and error correction codes like block codes, cyclic codes and convolution codes.

UNIT - I

Waveform Coding Techniques: PCM Generation and Reconstruction, Quantization Noise, Non Uniform Quantization and Compingand, DPCM, Adaptive DPCM, DM and Adaptive DM, Noise in PCM and DM.

UNIT - II

Error Control Codes
Linear Block Codes: Matrix Description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes. Cyclic Codes: Algebraic Structure, Encoding, Syndrome Calculation, Decoding. Convolution Codes: Encoding, Decoding,
UNIT - III


**Digital pass band transmission:** pass band transmission model, Gram-Schmidt orthogonalization procedure, Geometric interpretation of signals Coherent detection of signals in noise, probability of error, Correlation receiver.

UNIT - IV

**Digital Modulation Techniques:** Introduction, ASK, ASK Modulator, Coherent ASK Detector, Non-Coherent ASK Detector, FSK, Bandwidth and Frequency Spectrum of FSK, Non Coherent FSK Detector, Coherent FSK Detector, FSK Detection using PLL, BPSK, Coherent PSK Detection, QPSK, 8-PSK, 16-PSK  Differential PSK, QAM .

UNIT - V

**Spread Spectrum Modulation:** Use of Spread Spectrum, Direct Sequence Spread (DSSS), and Code Division Multiple Access, Ranging using DSSS, Frequency Hopping Spread Spectrum, PN - Sequence: Generation and characteristics, Synchronization in Spread Spectrum Systems.

TEXT BOOKS:

REFERENCES:
2. Electronic communication systems, Wayne Tomasi, 5 edition, Pearson
FUNDAMENTALS OF MANAGEMENT

B.Tech. III Year I Sem.                          L    T    P   C
Course Code: SM504MS                          3     0    0    3

Course Objective: To understand the Management Concepts, applications of Concepts in Practical aspects of business and development of Managerial Skills.

Course Outcome: The students understand the significance of Management in their Profession. The various Management Functions like Planning, Organizing, Staffing, Leading, Motivation and Control aspects are learnt in this course. The students can explore the Management Practices in their domain area.

UNIT - I

UNIT - II

UNIT - III

UNIT - IV
Leading and Motivation: Leadership, Power and Authority, Leadership Styles; Behavioral Leadership, Situational Leadership, Leadership Skills, Leader as Mentor and Coach, Leadership during adversity and Crisis; Handling Employee and Customer Complaints, Team Leadership.
Motivation - Types of Motivation; Relationship between Motivation, Performance and Engagement, Content Motivational Theories - Needs Hierarchy Theory, Two Factor Theory, Theory X and Theory Y.

UNIT - V
Controlling: Control, Types and Strategies for Control, Steps in Control Process, Budgetary and Non-Budgetary Controls. Characteristics of Effective Controls, Establishing control systems, Control frequency and Methods.

TEXT BOOKS:

REFERENCES:
LINEAR IC APPLICATIONS LAB

B.Tech. III Year I Sem.                                      L    T    P   C
Course Code: EC505PC                                      0     0    3    2

Note:
• To perform any twelve experiments
• Verify the functionality of the IC in the given application.

Design and Implementation of:
1. Inverting and Non-inverting Amplifiers using Op Amps.
4. Integrator Circuit using IC 741.
6. Active Filter Applications – LPF, HPF (first order)
7. IC 741 Waveform Generators – Sine, Square wave and Triangular waves.
11. IC 565 – PLL Applications.
12. Voltage Regulator using IC 723.
DIGITAL IC APPLICATIONS LAB

B.Tech. III Year I Sem.

Course Code: EC506PC

L T P C
0 0 3 2

Note:
- To perform any twelve experiments
- Verify the functionality of the IC in the given application.

Design and Implementation of:

1. Design a 16 x 4 priority encoder using two 8 x 3 priority encoder.
2. Design a 16 bit comparator using 4 bit Comparators.
3. Design a model to 53 counter using two decade counters.
4. Design a 450 KHz clock using NAND / NOR gates.
5. Design a 4 bit pseudo random sequence generator using 4 – bit ring counter.
6. Design a 16 x 1 multiplexer using 8 x 1 multiplexer.
7. Design a 16 bit Adder / Subtractor using 4 – bit Adder / Subtractor IC’s
8. Plot the transform Characteristics of 74H, LS, HS series IC’s.
9. Design a 4 – bit Gray to Binary and Binary to Gray Converter.
10. Design a two Digit 7 segment display unit using this display the Mod counter output of experiment 3.
13. Design a Ring counter and Twisted ring counter using a 4-bit shift register
14. Design a 4 digit hex counter using synchronous one digit hex counters.
15. Design a 4 digit hex counter using Asynchronous one digit hex counters.
DIGITAL COMMUNICATIONS LAB

B.Tech. III Year I Sem.                                      L    T    P   C
Course Code: EC507PC                          0     0    3    2

Note:
  • Perform any twelve experiments.
  • Hardware Testing to be done

List of Experiments:
  1. PCM Generation and Detection
  2. Differential Pulse Code Modulation
  3. Delta Modulation
  4. Adaptive Delta modulation
  5. Time Division Multiplexing of 2 Band Limited Signals
  6. Frequency Shift Keying: Generation and Detection
  7. Phase Shift Keying: Generation and Detection
  8. Amplitude Shift Keying: Generation and Detection
  9. Study of the spectral characteristics of PAM
10. Study of the spectral characteristics of PWM
11. Study of the spectral characteristics of QAM.
12. DPSK: Generation and Detection
13. QPSK: Generation and Detection
14. OFDM: Generation and Detection
PROFESSIONAL ETHICS

B.Tech. III Year I Sem. Course Code: MC500HS

Course Objective: To enable the students to imbibe and internalize the Values and Ethical Behaviour in the personal and Professional lives.

Course Outcome: The students will understand the importance of Values and Ethics in their personal lives and professional careers. The students will learn the rights and responsibilities as an employee, team member and a global citizen.

UNIT - I
Introduction to Professional Ethics: Basic Concepts, Governing Ethics, Personal & Professional Ethics, Ethical Dilemmas, Life Skills, Emotional Intelligence, Thoughts of Ethics, Value Education, Dimensions of Ethics, Profession and professionalism, Professional Associations, Professional Risks, Professional Accountabilities, Professional Success, Ethics and Profession.

UNIT - II

UNIT - III
Professional Practices in Engineering: Professions and Norms of Professional Conduct, Norms of Professional Conduct vs. Profession; Responsibilities, Obligations and Moral Values in Professional Ethics, Professional codes of ethics, the limits of predictability and responsibilities of the engineering profession. Central Responsibilities of Engineers - The Centrality of Responsibilities of Professional Ethics; lessons from 1979 American Airlines DC-10 Crash and Kansas City Hyatt Regency Walk away Collapse.

UNIT - IV
Work Place Rights & Responsibilities, Ethics in changing domains of Research, Engineers and Managers; Organizational Complaint Procedure, difference of Professional Judgment within the Nuclear Regulatory Commission (NRC), the Hanford Nuclear Reservation. Ethics in changing domains of research - The US government wide definition of research misconduct, research misconduct distinguished from mistakes and errors, recent history of attention to research misconduct, the emerging emphasis on understanding and fostering responsible conduct, responsible authorship, reviewing & editing.
UNIT - V

TEXT BOOKS:

REFERENCES
COMPUTER ORGANIZATION AND OPERATING SYSTEMS  
(Professional Elective – I)

B.Tech. III Year II Sem.  
Course Code: EC611PE  

Course Objectives: The course objectives are
- To have a thorough understanding of the basic structure and operation of a digital computer.
- To discuss in detail the operation of the arithmetic unit including the algorithms & implementation of fixed-point and floating-point addition, subtraction, multiplication & division.
- To study the different ways of communicating with I/O devices and standard I/O interfaces.
- To study the hierarchical memory system including cache memories and virtual memory.
- To demonstrate the knowledge of functions of operating system memory management scheduling, file system and interface, distributed systems, security and dead locks.
- To implement a significant portion of an Operating System.

Course Outcomes: Upon completion of the course, students will have thorough knowledge about:
- Basic structure of a digital computer
- Arithmetic operations of binary number system
- The organization of the Control unit, Arithmetic and Logical unit, Memory unit and the I/O unit.
- Operating system functions, types, system calls.
- Memory management techniques and dead lock avoidance operating systems' file system implementation and its interface.

UNIT - I


UNIT - II
**Micro Programmed Control:** Control Memory, Address Sequencing, Microprogram Examples, Design of Control Unit, Hard Wired Control, Microprogrammed Control.

**The Memory System:** Basic Concepts of Semiconductor RAM Memories, Read-Only Memories, Cache Memories Performance Considerations, Virtual99 Memories Secondary Storage, Introduction to RAID.

UNIT - III

UNIT - IV

**Memory Management:** Swapping, Contiguous Memory Allocation, Paging, Structure of The Page Table, Segmentation, Virtual Memory, Demand Paging, Page-Replacement Algorithms, Allocation of Frames, Thrashing Case Studies - UNIX, Linux, Windows

**Principles of Deadlock:** System Model, Deadlock Characterization, Deadlock Prevention, Detection and Avoidance, Recovery from Deadlock.

UNIT - V

**File System Implementation:** File System Structure, File System Implementation, Directory Implementation, Allocation Methods, Free-Space Management.

**TEXT BOOKS:**

**REFERENCE BOOKS:**
DIGITAL IMAGE PROCESSING
(Professional Elective – I)

B.Tech. III Year II Sem. Course Code: EC612PE

Course Objectives:
- To comprehend the relation between human visual system and machine perception and processing of digital images.
- To provide a detailed approach towards image processing applications like enhancement, segmentation, and compression.

Course Outcomes:
- Exploration of the limitations of the computational methods on digital images.
- Expected to implement the spatial and frequency domain image transforms on enhancement and restoration of images.
- Elaborate understanding on image enhancement techniques.
- Expected to define the need for compression and evaluate the basic compression algorithms.

UNIT - I

UNIT - II
Image Enhancement (Frequency Domain): Filtering in Frequency Domain, Low Pass (Smoothing) and High Pass (Sharpening) Filters in Frequency Domain.

UNIT - III

UNIT – IV
Image Segmentation: Detection of Discontinuities, Edge Linking And Boundary Detection, thresholding, Region Oriented Segmentation.
Morphological Image Processing: Dilation and Erosion: Dilation, Structuring Element Decomposition, Erosion, Combining Dilation and Erosion, Opening and Closing, Hit or Miss Transformation.
UNIT - V

**Image Compression:** Redundancies and their Removal Methods, Fidelity Criteria, Image Compression Models, Huffman and Arithmetic Coding, Error Free Compression, Lossy Compression, Lossy and Lossless Predictive Coding, Transform Based Compression, JPEG 2000 Standards.

**TEXT BOOKS:**

**REFERENCE BOOKS:**
SPREAD SPECTRUM COMMUNICATIONS
(Professional Elective – I)

B.Tech. III Year II Sem.
Course Code: EC613PE

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Course Objectives: The objectives of this course are to make the student
- Understand the concept of Spread Spectrum and study various types of Spread spectrum sequences and their generation.
- Understand the principles of Code Division Multiple Access (CDMA) and use of Spread spectrum concept in CDMA
- Understand various Code tracing loops for optimum tracking of wideband signals viz spread spectrum signals
- Understand the procedure for synchronization of receiver for receiving the Spread spectrum signal.
- Study the performance of spread spectrum systems in Jamming environment, systems with Forward Error Correction and Multiuser detection in CDMA cellular radio.

Course Outcomes: On completion of this course student will be able to
- Generate various types of Spread spectrum sequences and can simulate CDMA system (Both Transmitter & Receiver).
- Analyze the performance of Spread spectrum systems in Jamming environment and systems with Forward Error Correction.
- Can provide detection and cancellation schemes for Multiusers in CDMA cellular radio.

UNIT - I
Binary Shift Register Sequences for Spread Spectrum Systems: Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT - II
UNIT - III
Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT - IV

UNIT - V

TEXT BOOKS:

REFERENCE BOOKS:
DIGITAL SYSTEM DESIGN  
(Professional Elective – I)

B.Tech. III Year II Sem.  
Course Code: EC614PE  
Course Code: EC614PE  
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Course Objectives:
- To provide extended knowledge of digital logic circuits in the form of state model approach.
- To provide an overview of system design approach using programmable logic devices.
- To provide and understand of fault models and test methods.

Course Outcomes:
- To understands the minimization of Finite state machine.
- To exposes the design approaches using ROM’s, PAL’s and PLA’s.
- To provide in depth understanding of Fault models.
- To understands test pattern generation techniques for fault detection.
- To design fault diagnosis in sequential circuits.

UNIT - I

UNIT - II
Digital Design: Digital Design Using ROMs, PALs and PLAs, BCD Adder, 32 – bit adder, State graphs for control circuits, Scoreboard and Controller, A shift and add multiplier, Array multiplier, Keypad Scanner, Binary divider.

UNIT - III
SM Charts: State machine charts, Derivation of SM Charts, Realization of SM Chart, Implementation of Binary Multiplier, dice game controller.

UNIT - IV:
PODEM, Random testing, Transition count testing, Signature analysis and test bridging faults.

UNIT - V
Fault Diagnosis in Sequential Circuits: Circuit Test Approach, Transition Check Approach – State identification and fault detection experiment, Machine identification, Design of fault detection experiment

TEXT BOOKS:

REFERENCE BOOKS:
ANTENNAS AND WAVE PROPAGATION

B.Tech. III Year II Sem. 
Course Code: EC601PC

Course Objectives: This can be termed a middle level course in the electronic communication engineering domain. The course deals with antenna basics, different types of antennas, some design features, antenna measurements and wave propagation, and has the following main objectives:

- To understand the concept of radiation, antenna definitions and significance of antenna parameters, to derive and analyze the radiation characteristics of thin wire dipole antennas and solve numerical problems.
- To distinguish between UHF, VHF and Microwave Antennas, their requirements, specifications, characteristics and design relations.
- To analyze the characteristics of yagi-uda antennas, helical antennas, pyramidal horns, microstrip patch antennas and parabolic reflectors and identify the requirements to facilitate their design.
- To identify the antenna array requirements, to determine the characteristics of ULAs and estimate the patterns of BSA, EFA, and Binomial Arrays.
- To understand the concepts and set-up requirements for microwave measurements, and familiarize with the procedure to enable antenna measurements.
- To define and distinguish between different phenomenon of wave propagation (ground wave, space wave and sky wave), their frequency dependence, and estimate their characteristics, identifying their profiles and parameters involved.

Course Outcomes: Having gone through this course on Antenna Theory and Techniques, and Wave Propagation, the students would be able to:

- Explain the mechanism of radiation, distinguish between different antenna characteristic parameters, establish their mathematical relations, estimate them for different practical cases.
- Distinguish between short dipoles, half-wave dipoles, quarter-wave monopoles and small loops, configure their current distributions, derive their far fields and radiation characteristics and sketch their patterns.
- Characterize the antennas based on frequency, configure the geometry and establish the radiation patterns of folded dipole, Yagi-Uda Antenna, Helical Antennas, Horn Antennas, and to acquire the knowledge of their analysis, design and development.
- Analyze a microstrip rectangular patch antenna and a parabolic reflector antenna, identify the requirements and relevant feed structure, carry out the design and establish their patterns.
- Specify the requirements for microwave measurements and arrange a setup to carry out the antenna far zone pattern and gain measurements in the laboratory.
• Carry out the Linear Array Analysis, estimate the array factor and characteristics and sketch the pattern for 2-element array, N-element BSA, EFA, modified EFA, Binomial Arrays.

• Classify the different wave propagation mechanisms, identify their frequency ranges, determine the characteristic features of ground wave, ionospheric wave, space wave, duct and tropospheric propagations, and estimate the parameters involved.

UNIT - I
Antenna Basics: Introduction, Basic Antenna Parameters – Patterns, Beam Area, Radiation Intensity, Beam Efficiency, Directivity-Gain-Resolution, Antenna Apertures, Effective Height, Illustrative Problems.
Fields from Oscillating Dipole, Field Zones, Front - to-back Ratio, Antenna Theorems, Radiation, Retarded Potentials – Helmholtz Theorem
Thin Linear Wire Antennas – Radiation from Small Electric Dipole, Quarter Wave Monopole and Half Wave Dipole – Current Distributions, Field Components, Radiated Power, Radiation Resistance, Beam Width, Directivity, Effective Area and Effective Height, Natural Current Distributions, Far Fields and Patterns of Thin Linear Centre-fed Antennas of Different Lengths, Illustrative Problems. Loop Antennas - Introduction, Small Loop, Comparison of Far Fields of Small Loop and Short Dipole, Radiation Resistances and Directivities of Small Loops (Qualitative Treatment).

UNIT - II

UNIT - III

UNIT - IV
Antenna Arrays: Point Sources – Definition, Patterns, arrays of 2 Isotropic Sources - Different Cases, Principle of Pattern Multiplication, Uniform Linear Arrays – Broadside Arrays, End fire Arrays, EFA with Increased Directivity, Derivation of their Characteristics and Comparison, BSAs with Non-uniform Amplitude Distributions – General Considerations and Binomial Arrays, Illustrative Problems.
**Antenna Measurements:** Introduction, Concepts - Reciprocity, Near and Far Fields, Coordinate System, Sources of Errors. Patterns to be Measured, Directivity Measurement, Gain Measurements (by Comparison, Absolute and 3-Antenna Methods)

**UNIT - V**


**Wave Propagation – II:** Sky Wave Propagation – Introduction, Structure of Ionosphere, Refraction and Reflection of Sky Waves by Ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation between MUF and Skip Distance, Multi-hop Propagation.

**TEXT BOOKS:**

**REFERENCE BOOKS:**
MICROPROCESSORS AND MICROCONTROLLERS

B.Tech. III Year II Sem.                                      L    T    P   C
Course Code: EC602PC                          4     0    0    4

Course Objectives:
- To develop an understanding of the operations of microprocessors and micro controllers; machine language programming and interfacing techniques.

Course Outcomes:
- Understands the internal architecture and organization of 8086, 8051 and ARM processors/controllers.
- Understands the interfacing techniques to 8086 and 8051 and can develop assembly language programming to design microprocessor/ micro controller based systems.

UNIT - I
Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, and Simple Programs involving Logical, Branch and Call Instructions, Sorting, String Manipulations.

UNIT - II
Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051.
8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

UNIT – III
I/O And Memory Interface: LCD, Keyboard, External Memory RAM, ROM Interface, ADC, DAC Interface to 8051.
Serial Communication and Bus Interface: Serial Communication Standards, Serial Data Transfer Scheme, On board Communication Interfaces-I2C Bus, SPI Bus, UART; External Communication Interfaces-RS232,USB.

UNIT – IV
ARM Architecture: ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.
UNIT – V

TEXT BOOKS:

REFERENCE BOOKS:
2. Introduction to Embedded Systems, Shibu K.V, MHE, 2009
DIGITAL SIGNAL PROCESSING

B.Tech. III Year II Sem.                                      L    T    P   C
Course Code: EC603PC                          4     0    0    4

Course Objectives: This course is an essential course that provides design techniques for processing all type of signals in various fields. The main objectives are:

- To provide background and fundamental material for the analysis and processing of digital signals.
- To familiarize the relationships between continuous-time and discrete time signals and systems.
- To study fundamentals of time, frequency and Z-plane analysis and to discuss the inter-relationships of these analytic method.
- To study the designs and structures of digital (IIR and FIR) filters from analysis to synthesis for a given specifications.
- The impetus is to introduce a few real-world signal processing applications.
- To acquaint in FFT algorithms, Multi-rate signal processing techniques and finite word length effects.

Course Outcomes: On completion of this subject, the student should be able to:

- Perform time, frequency, and Z-transform analysis on signals and systems.
- Understand the inter-relationship between DFT and various transforms.
- Understand the significance of various filter structures and effects of round off errors.
- Design a digital filter for a given specification.
- Understand the fast computation of DFT and appreciate the FFT processing.
- Understand the tradeoffs between normal and multi rate DSP techniques and finite length word effects.

UNIT - I


UNIT - II
**Fast Fourier Transforms:** Fast Fourier Transforms (FFT) - Radix-2 Decimation-in-Time and Decimation-in-Frequency FFT Algorithms, Inverse FFT, and FFT with General Radix-N.

**UNIT - III**
**IIR Digital Filters:** Analog filter approximations – Butterworth and Chebyshev, Design of IIR Digital Filters from Analog Filters, Step and Impulse Invariant Techniques, Bilinear Transformation Method, Spectral Transformations.

**UNIT - IV**
**FIR Digital Filters:** Characteristics of FIR Digital Filters, Frequency Response, Design of FIR Filters: Fourier Method, Digital Filters using Window Techniques, Frequency Sampling Technique, Comparison of IIR & FIR filters.

**UNIT - V**
**Multirate Digital Signal Processing:** Introduction, Down Sampling, Decimation, Upsampling, Interpolation, Sampling Rate Conversion, Conversion of Band Pass Signals, Concept of Resampling, Applications of Multi Rate Signal Processing.

**Finite Word Length Effects:** Limit cycles, Overflow Oscillations, Round-off Noise in IIR Digital Filters, Computational Output Round off Noise, Methods to Prevent Overflow, Trade off between Round Off and Overflow Noise, Measurement of Coefficient Quantization Effects through Pole-Zero Movement, Dead Band Effects.

**TEXT BOOKS:**

**REFERENCES:**
DIGITAL SIGNAL PROCESSING LAB

B.Tech. III Year II Sem.                                      L   T   P   C
Course Code: EC604PC                                      0   0   3   2

Note:
1. The Programs shall be implemented in Software (Using MATLAB / Lab View / C Programming/ Equivalent) and Hardware (Using TI / Analog Devices / Motorola / Equivalent DSP processors).
2. Minimum of 12 experiments to be conducted.

List of Experiments
1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations
3. To find DFT / IDFT of given DT Signal
4. To find Frequency Response of a given System given in Transfer Function/ Differential equation form.
5. Obtain Fourier series coefficients by formula and using FET and compare for half sine wave.
6. Implementation of FFT of given Sequence
7. Determination of Power Spectrum of a given Signal(s).
8. Implementation of LP FIR Filter for a given Sequence/Signal.
9. Implementation of HP IIR Filter for a given Sequence/Signal
10. Generation of Narrow Band Signal through Filtering
11. Generation of DTMF Signals
12. Implementation of Decimation Process
13. Implementation of Interpolation Process
14. Implementation of I/D Sampling Rate Converters
15. Impulse Response of First order and Second Order Systems.
MICROPROCESSORS AND MICROCONTROLLERS LAB

B.Tech. III Year II Sem.                                      L    T    P   C
Course Code: EC605PC                          0     0    3    2

Note: - Minimum of 12 experiments to be conducted.
The following programs/experiments are to be written for assembler and to be executed the
same with 8086 and 8051 kits.

List of Experiments:
1. Programs for 16 bit arithmetic operations 8086(using various addressing modes)
2. Programs for sorting an array for 8086.
3. Programs for searching for a number of characters in a string for 8086.
4. Programs for string manipulation for 8086.
5. Programs for digital clock design using 8086.
6. Interfacing ADC and DAC to 8086.
7. Parallel communication between two microprocessor kits using 8255.
8. Serial communication between two microprocessor kits using 8251.
9. Interfacing to 8086 and programming to control stepper motor.
11. Program and verify Timer/Counter in 8051.
12. Program and verify interrupt handling in 8051.
13. UART operation in 8051.
14. Communication between 8051 kit and PC
15. Interfacing LCD to 8051
16. Interfacing Matrix/Keyboard to 8051
17. Data transfer from peripheral to memory through DMA controller 8237/8257
ADVANCED ENGLISH COMMUNICATION SKILLS (AECS) LAB

B.Tech. III Year II Sem.                                      L    T    P   C
Course Code: EN606HS                          0     0    3    2

Introduction
A course on Advanced English Communication Skills (AECS) Lab is considered essential at the third year level of B.Tech and B.Pharmacy courses. At this stage, the students need to prepare themselves for their career which requires them to listen to, read, speak and write in English both for their professional and interpersonal communication. The main purpose of this course is to prepare the students of Engineering for their placements.

Course Objectives: This Lab focuses on using multi-media instruction for language development to meet the following targets:
- To improve students’ fluency in spoken English
- To enable them to listen to English spoken at normal conversational speed
- To help students develop their vocabulary
- To read and comprehend texts in different contexts
- To communicate their ideas relevantly and coherently in writing
- To make students industry-ready
- To help students acquire behavioural skills for their personal and professional life
- To respond appropriately in different socio-cultural and professional contexts

Course Outcomes: Students will be able to:
- Acquire vocabulary and use it contextually
- Listen and speak effectively
- Develop proficiency in academic reading and writing
- Increase possibilities of job prospects
- Communicate confidently in formal and informal contexts

Syllabus
The following course activities will be conducted as part of the Advanced English Communication Skills (AECS) Lab:

2. **Reading Comprehension** – General Vs Local Comprehension, Reading for Facts, Guessing Meanings from Context, , Skimming, Scanning, Inferring Meaning.
4. **Presentation Skills** – Oral Presentations (individual or group) through JAM Sessions/Seminars/PPTs and Written Presentations through Posters/Projects/Reports/ e-mails/Assignments… etc.,
5. **Group Discussion and Interview Skills** – Dynamics of Group Discussion, Intervention, Summarizing, Modulation of Voice, Body Language, Relevance, Fluency and Organization of Ideas and Rubrics of Evaluation- Concept and Process,
Pre-interview Planning, Opening Strategies, Answering Strategies, Interview through Tele-conference & Video-conference and Mock Interviews.

**Minimum Hardware Requirement:**
Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics
- Eight round tables with five movable chairs for each table.
- Audio-visual aids
- LCD Projector
- Public Address system
- Computer with suitable configuration

**Suggested Software:** The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner’s Compass, 8th Edition
- DELTA’s key to the Next Generation TOEFL Test: Advanced Skill Practice.

**REFERENCES:**